

INTERFERENCE

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L34	1	((fpga or field adj programmable adj gate adj array) and (clb or configurable adj logic adj block) same address) and location and register and map\$4 and (functional same block)).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:28

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	931	(fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L2	241	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same function) and map\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L3	164	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same function) and map\$4 and address and configur\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L4	36	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same function) and map\$4 and address and configur\$4 and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L5	159	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same function) and map\$4 and address and configur\$4 and stor\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L6	135	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same function) and map\$4 and address and configur\$4 and stor\$3 and register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L7	57	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same function) and (map\$4 same address) and configur\$4 and stor\$3 and register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L8	28	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same function) and map\$4 and address and configur\$4 and register and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12

L9	69	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same function) and map\$4 and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L14	152	(fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L15	24	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same address) and map\$4 and (functional same block)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:10
L16	4646	address same configur\$4 same map\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L17	24	(fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) and (address same configur\$4 same map\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L18	7	(fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same address same map\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L19	80	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same address) and map\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L20	968	address same configur\$4 same map\$4 same register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:24

L21	118	address same configur\$4 same map\$4 same register and fpga	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:24
L22	23299	address same configur\$4 same map\$4 same register and fpga and "716"/\$.ccls. or "326"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L23	11	address same configur\$4 same map\$4 same register and fpga and ("716"/\$.ccls. or "326"/\$.ccls.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L24	30	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same address) and (map\$4 same register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L25	28743	(fpga or field adj programmable adj gate adj array) and (clb or configurable adj logic adj block)(map\$4 same address) and register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 10:12
L26	88	(fpga or field adj programmable adj gate adj array) and (clb or configurable adj logic adj block) and (map\$4 same address) and register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:25
L27	4	((fpga or field adj programmable adj gate adj array) same (clb or configurable adj logic adj block) same address) and map\$4 and (functional same block) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:27
L28	1269	(716/12-14).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:24

L29	0	l28 and (address same configur\$4 same map\$4 same register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:25
L30	0	l28 and (fpga or field adj programmable adj gate adj array) and (clb or configurable adj logic adj block) and (map\$4 same address) and register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:25
L31	0	(fpga or field adj programmable adj gate adj array) and (clb or configurable adj logic adj block) and (map\$4 same address) and register and (716/12-14).ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:26
L32	16	(fpga or field adj programmable adj gate adj array) and (clb or configurable adj logic adj block) and (map\$4 same address) and register and "716"/.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:26
L33	6	((fpga or field adj programmable adj gate adj array) and (clb or configurable adj logic adj block) same address) and location and register and map\$4 and (functional same block) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:28
L34	1	((fpga or field adj programmable adj gate adj array) and (clb or configurable adj logic adj block) same address) and location and register and map\$4 and (functional same block)).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/31 11:28

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